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Filed 1/9/2002
Attorney docket no. BEA920000019US1

In the claims:

1. (currently amended) A method for handling operations within a hardware device, comprising:

providing within the device information regarding ~~the~~ an operation, the operation having a predetermined responsive output as encoded within a transaction lookup table, the provided information including information identifying the operation;

selecting at least some of the identifying information of the operation to output to a comparator and the transaction lookup table, and output of the comparator and output of the transaction lookup table are input into a multiplexer;

selecting an alternative responsive output for the operation instead of the predetermined responsive output based upon the selected identifying information resulting in the comparator directing the multiplexer to output the alternative responsive output, such that the multiplexer effectively ~~converting~~s at least some of the information regarding the operation based upon the selected identifying information; and

executing the operation based upon the converted information.

2. (original) The method of claim 1, wherein the provided information is within a register of the device.

3. (original) The method of claim 1, wherein the identifying information is within a register of the device.

4. (original) The method of claim 1, wherein the converted information is within a register of the device.

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5. (currently amended) The method of claim 1, wherein the step of providing information
regarding the operation comprises <u>providing the predetermined responsive output and the</u>
<u>alternative responsive output</u>[[:]]
~~loading operation identifications; and~~
~~generating, based on the operation identifications, a range of operations related to the~~
~~provided information~~.

6. (cancelled)

7. (original) The method of claim 5, wherein the operation identifications comprise fields for
operation identification, length, attribute and target of each operation.

8. (currently amended) A method for redirecting transactions within a hardware device, wherein
transactions occurring within said device contain fields of information regarding the transaction,
the method comprising the steps of:
    loading all of said fields necessary to identify a transaction into a first register;
    selecting which fields of said first register are to be acted upon <u>and inputting the selected</u>
<u>fields into a multiplexer;</u>
    converting the transaction information to be redirected through a pre-programmed value for
each said field <u>by inputting into the multiplexer a predetermined responsive value into the</u>
<u>multiplexer, the multiplexer also receiving input from a comparator, such that the multiplexer</u>
<u>outputs an alternative responsive value for the transaction</u>; and
    outputting said new transaction results to a register.

9. (currently amended) The method of claim 8, wherein the step of loading said field necessary
to identify a transaction include<u>s</u> first loading transaction identifications~~, then operating on said~~

~~transaction identifications to generate a range of transactions related to one or more of said fields of information~~.

10. (original) The method of claim 8, wherein said fields of information are comprised of a field for transaction identification, length, attribute and target of each transaction.

11. (currently amended) A method for redirecting operations within a hardware device, wherein operations occurring within said device contain fields of information regarding the operation and such operations are compared with a preprogram<u>m</u>ed list of responses and the hardware device issues responses based on each operation, the method comprising the steps of:

    creating a list of identified operations for which a redirected response is desired;

    comparing ~~each~~ <u>an</u> operation with the list of said identified operations <u>using a comparator</u>;

    <u>outputting results of the comparator and a preprogrammed response from the preprogrammed list of responses for the operation into a multiplexer, such that output of the multiplexer represents the redirected response for the operation;</u> and

    substituting the redirected response for the <u>preprogrammed</u> response from said programmed list of responses.

12. (currently amended) The method of claim 11, wherein the step <u>of</u> creating a list of identified operations include<u>s</u> first loading transaction identification~~, then operating on said operation identifications to generate a range of operations related to one or more of said fields of information~~.

13. (original) The method of claim 11, wherein said fields of information are comprised of a field for transaction identification, length, attribute and target of each transaction.

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14. (currently amended) In a data processing system in which a given operation results in a
predetermined response, a system for altering such predetermined response comprised of:

   first storage means to identify operations for which a response different from said
predetermined response is desired;

   comparator means to compare said given operation with said identified operations;

   second storage means to load a substitute response for said predetermined response, the
second storage means comprising a plurality of registers; and

   selection means to select said substitute response when a given operation meets a predefined
criteria for substituting a response from said second [register] storage means, the selection means
comprising a multiplexer into which the predetermined response is input and output from the
plurality of registers is input, such that output of the comparator means is employed to select the
output of the plurality of registers in lieu of the predetermined response as the substitute response.

15. (original) The system of claim 14, wherein one or more of said storage means may be
selectively enabled or disabled.

16. (currently amended) In a data processing system utilizing a hardware control device in which
a given operation results in a predetermined response for that operation, a system for providing a
programmable redefinition of allowed instructions and associated responses within said hardware
device including:

   first register means which contains fields to identify preselected operations which may occur
within the system;

   second register means which operates upon selected fields in the first register means to further
define a criteria related-to-a-range-of-operations for which redirecting a response is desired;

   comparator means which compares the identified operations with a current operation and
selects a substitute value when said identified operation meets a said defined criteria;

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transaction lookup table means to output a standard value for the current operation; and

multiplexer means receiving input from the comparator means and the transaction lookup

table means and outputting a substitute value for the current operation

~~third register means which contain substitute values for all said operations which meet said~~

~~defined criteria.~~

17. (original) The system of claim 16, wherein one or more of said register means may be

selectively enabled or disabled.

18. (currently amended) A data processing system for executing an operation, comprising:

an identification store including information identifying at least selected operations;

a comparator responsive to the operation and the identifying information; ~~and~~

a substitute ~~operation~~ value responsive to the comparator and the operation;

a standard value responsive to the comparator and the operation; and

a multiplexer into which the substitute value, the standard value, and output from the

comparator are input, and that outputs one of the substitute value and the standard value based on

the output from the comparator.

19. (original) The system of claim 18, wherein the comparator is responsive to a mask of the

identifying information.